

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Colgan et al.

SERIAL NO.: 10/789,500

FILED: February 27, 2004

FOR: APPARATUS AND METHODS FOR COOLING
SEMICONDUCTOR INTEGRATED CIRCUIT PACKAGE
STRUCTURES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF REPLACEMENT DRAWINGS

Sir:

Applicant submits herewith ten (11) sheets of formal drawings
depicting FIGS. 1-10 for this application.

Respectfully submitted,



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